

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 16073404	FILING DATE 02/11/2002	CLASS 257	SUBCLASS 341	CAU 2341	EXAMINER Mandala
**APPLICANTS: Chien Feng-Tso					
**CONTINUING DATA VERIFIED:					
**PRECEDENT APPLICATIONS VERIFIED:					
PG-FVS <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>			RESCIND <input type="checkbox"/>		
Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO		
Continuing conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			25740-02A		
Ver. and Acknowledged Examiners's initials					
This is a MOSFET device with reduced channel-back and being capable of increasing avalanche-breakdown current endurance, and method of manufacturing the same					
U.S. DEPT. OF COMMERCE PAT. & TM. PTO-4361 (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drawg.	Figs. Drawg.
ISSUE FEE		Print Fig.	
Amount Due	Date Paid	Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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